

Spring 2019

## ECE 2274 ELECTRONIC NETWORKS LAB SYLLABUS

**Lab Supervisor: Richard Cooper**

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**Lab Instructor:**

### 1. Course Material:

Laboratory Projects will be available at <http://www.courses.ece.vt.edu/ece2274/> Bring a **flash drive** and (optional) your proto board from ECE2074 with you to class.

### 2. Lab Objectives:

- To gain practical understanding of circuit theory and electronic components.
- To become familiar with basic laboratory equipment.
- To become proficient using a circuit simulator.

### 3. Grading:

Grade will be divided as follows: All late work must submitted prior to the Final Exam.

Pre-lab	55%
Lab Report	30%
Final Exam	15%

A 100% to 93%	A- < 93% to 90%	
B+ < 90% to 87%	B < 87% to 83%	B- < 83% to 80%
C+ < 80% to 77%	C < 77% to 73%	C- < 73% to 70%
D+ < 70% to 67%	D < 67% to 63%	D- < 63% to 60%
F < 60% to 59%		

**4. Pre-Labs:** Each student is required to **individually complete handwritten pre-labs** include **schematics and labeled plots** and turn in the pre-lab include CRN, and bench #. Failure to turn in the pre-lab will result in a "0" grade for that particular pre-lab. Late pre-labs will be accepted with prior approval with a 5 point per day penalty. Some pre-labs are lengthy; so do not wait until the last minute to start the pre-labs.

**5. Lab Reports:** You Must return all cables, connector and components to their proper location or **receive 10 point reduction. One lab report per group include CRN, and Bench #**, each lab report must be completed at the scheduled lab period. Missing a scheduled lab without prior approval will result in a 5 point penalty per day for that particular lab.

**6. Circuit simulator: LTspice** Since a circuit simulator is required to complete many of the pre-labs. You must include **schematics and labeled plots**, **YOU MUST BE PROFICIENT IN a circuit simulator.**

**7. Honor Code:** The Honor Code will be enforced in this class. Discussion of topics with other people in the class is fine, but all submitted work should be your own.